

VIA FACSIMILE (703) 872-9306

9D-RG-19394-Daum  
PATENTIN THE SPECIFICATION

Please replace the paragraph beginning on page 5, line 20, ending on page 6, line 3 and amended by the amendment mailed on May 7, 2004 with the following amended paragraph.

A more detailed sequence of program steps for the initiation of time code transmission is given in ~~Figures 4 and 6.~~ Figures 4 and 5. In one exemplary embodiment subroutine 62 is added to the internal clock update routine that incorporates the programming steps below. In an alternate exemplary embodiment the programming steps are added to the clock update code without executing a subroutine.

Please replace the paragraph beginning on page 6, line 25, ending on page 7, line 9 with the following amended paragraph.

An exemplary implementation of process 50 (Figure 3) that received the data from communication module 20 and forwards it to a routine for time and date setting is described below, ~~and as and is~~ illustrated in Figures 6 and 7. It is understood that process 50 can be implemented by a subroutine call of as part of the main program 122 (Figure 7). In an exemplary embodiment the data transfer from communication module 20 is initiated by communication module 20 by use of an interrupt to the main micro processor 24. In one example, the interrupt is initiated on reset of electronic device 12, ~~which~~ 12, which may include power up reset. After receipt of the time ~~or data~~ or date information from another appliance, communication module 20 sends an interrupt signal 23 to microprocessor 24. Microprocessor 24 interrupts normal operation and services the interrupt. Microprocessor 24 reads the date or time code variable and writes it to the appropriate clock variable memory location. Microprocessor 24 then continues to execute the interrupted program. It is understood that the data can be presented as shown above by type followed by type data content as specific expected sequences

**VIA FACSIMILE (703) 872-9306**

**9D-RG-19394-Daum  
PATENT**

of time and / or date, or by a combined type and data content. In an alternate exemplary embodiment the microprocessor algorithm continuously queries communication module 20 for the presence of new data as part of the main control loop and then writes the new data to the relevant time and / or date variable.